

A DRIVE CONTROL SYSTEM FOR A FIBER-BASED PLASMA DISPLAY

REFERENCE TO RELATED APPLICATIONS

This is a divisional patent application of copending application serial number 09/299,370, filed April 26, 1999, ^{now U.S. Pat. # 6,414,433} entitled "FIBER-BASED PLASMA DISPLAY". The
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aforementioned application is hereby incorporated herein by reference.

FIELD OF THE INVENTION

The invention pertains to the field of fiber-based displays and methods of manufacture. More particularly, the invention pertains to fiber-based full-color plasma displays.

BACKGROUND OF THE INVENTION

All electronic display technologies are composed of a large array of display picture elements, called pixels arranged in a two-dimensional matrix. Color is added to these displays by subdividing each pixel element into three-color subpixels. The electronic display technologies can be further divided into a category known as flat-panel displays. The basic structure of a flat-panel display comprises two glass plates with a conductor pattern of electrodes on the inner surfaces of each plate with additional structure to separate the plates or create a channel. The conductors are configured in a x-y matrix with horizontal and vertical electrodes deposited at right angles from each other to allow for matrix addressing. Examples of flat-panel displays include plasma displays, plasma addressed liquid crystal (PALC) displays, field emission displays (FED), and the like.

Plasma display panels (PDP) have been around for about 30 years, however they have not seen widespread commercial use. The main reasons are the short lifetime, low efficiency, and cost of the color plasma displays. Most of the performance issues were solved with the invention of the three electrode surface discharge AC plasma display (G.W. Dick, "Three-Electrode per PEL AC Plasma Display Panel", 1985 International Display Research Conf., pp. 45-50; U.S. Pat. Nos. 4,554,537, 4,728,864, 4,833,463, 5,086,297, 5,661,500, and 5,674,553). The new three electrode surface discharge structure